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Patent  
10/790,615

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**IN THE CLAIMS**

Please cancel Claims 28-33 without prejudice and without disclaimer of subject matter.

1-13. (Cancelled).

14. (Original) A method for processing a plurality of signals, comprising: converting an analog signal to a desired format; converting a digital signal to the desired format; demultiplexing a third signal in the desired format, said third signal having an audio component and a video component; packetizing the first, second and third signals; and multiplexing the first, second and third signals into a single transport stream.

15. (Original) The method according to claim 14, further comprising: storing the single transport stream.

16. (Original) The method according to claim 14, further comprising buffering the first, second and third signals prior to the packetizing.

17. (Original) The method according to claim 14, wherein said converting the analog signal comprises: demodulating the analog signal; decoding the analog signal to a predetermined format; converting the analog signal in the predetermined format to a digital signal; and encoding the digital signal.

18. (Original) The method according to claim 17, wherein the desired format comprises an MPEG format.

19. (Original) The method according to claim 14, further comprising routing the analog signal and the digital signal from a single device to one or more selected devices for the converting.

Patent  
10/790,615

20. (Original) The method according to claim 14, further comprising a selector to select an analog signal, a digital signal and the third signal from among the plurality of signals.
21. (Original) An apparatus for processing a plurality of signals comprising: a first converter to convert an analog signal among the plurality of signals to a desired format; a second converter to convert a digital signal among the plurality of signals to the desired format; a demultiplexer to demultiplex a third signal in the desired format among the plurality of signals, said third signal having an audio component and a video component; a packetizer coupled to the demultiplexer, and the first and second converters, said packetizer to packetize the first, second and third signals; and a formatter coupled to the packetizer, said formatter to multiplex the first, second and third signals into a single transport stream.
22. (Original) The apparatus according to claim 21, further comprising: a source interface having one or more input terminals to receive the plurality of signals of a plurality of different formats.
23. (Original) The apparatus according to claim 21, further comprising: a storage coupled to the formatter to store the single transport stream.
24. (Original) The apparatus according to claim 22, further comprising: a selector coupled to the source interface, the demultiplexer, and the first and second converters, said selector to select which of the plurality of signals are sent to each of the demultiplexer and the first and second converters.
25. (Original) The apparatus according to claim 21, further comprising: a buffer coupled between the first and second converters and the packetizer.

Patent  
10/790,615

26. (Original) The apparatus according to claim 21, wherein the first converter comprises: a demodulator; a decoder coupled to the demodulator; an analog-to-digital converter coupled to the demodulator; and an encoder coupled between the analog-to-digital converter and the packetizer.

27. (Original) The apparatus according to claim 26, wherein the encoder comprises an MPEG encoder.

28-33. (Canceled)